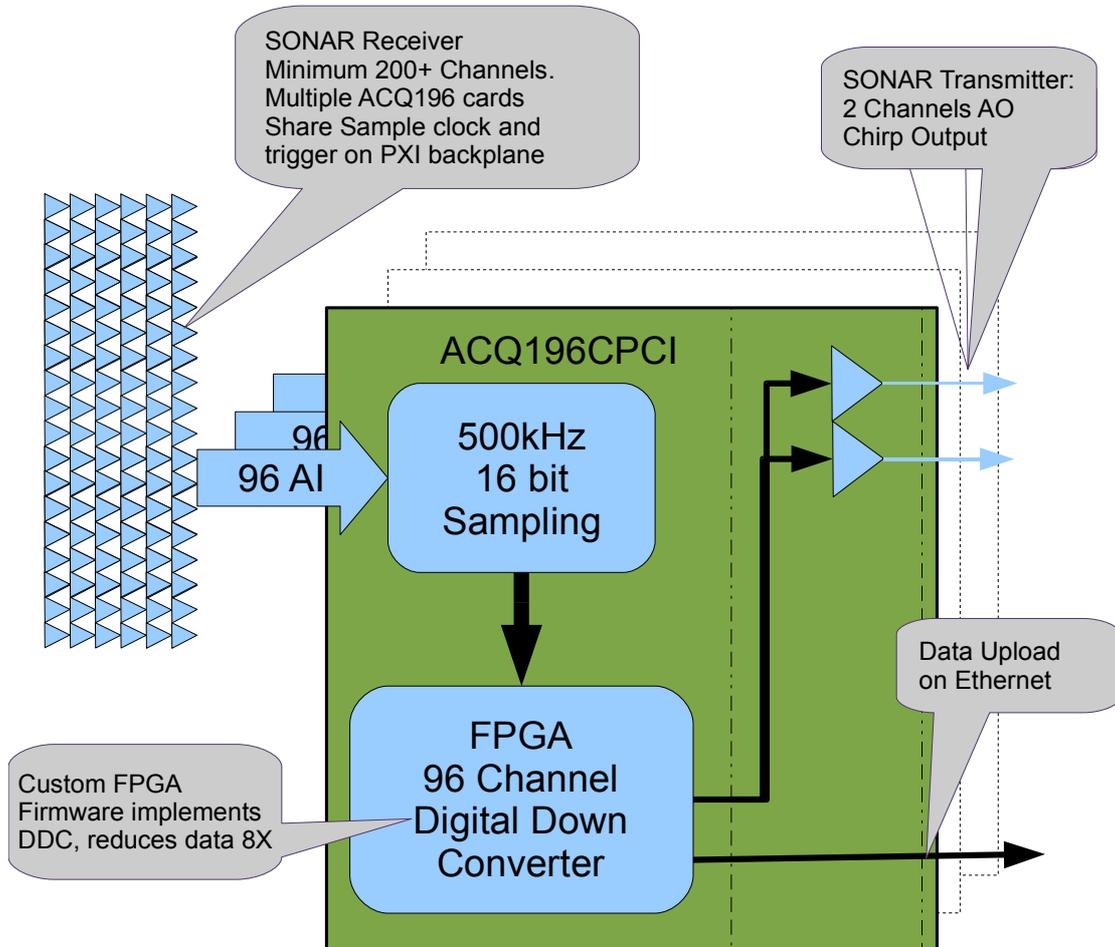


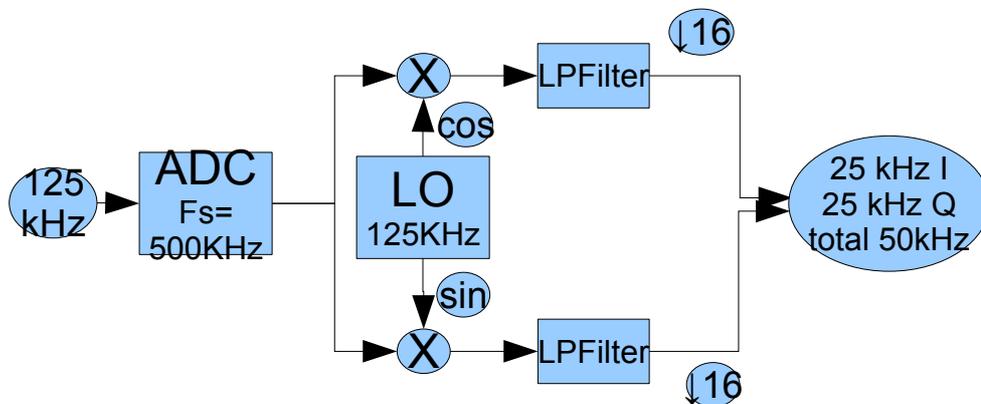
Sonar System with 96 Channel DDC

Concept

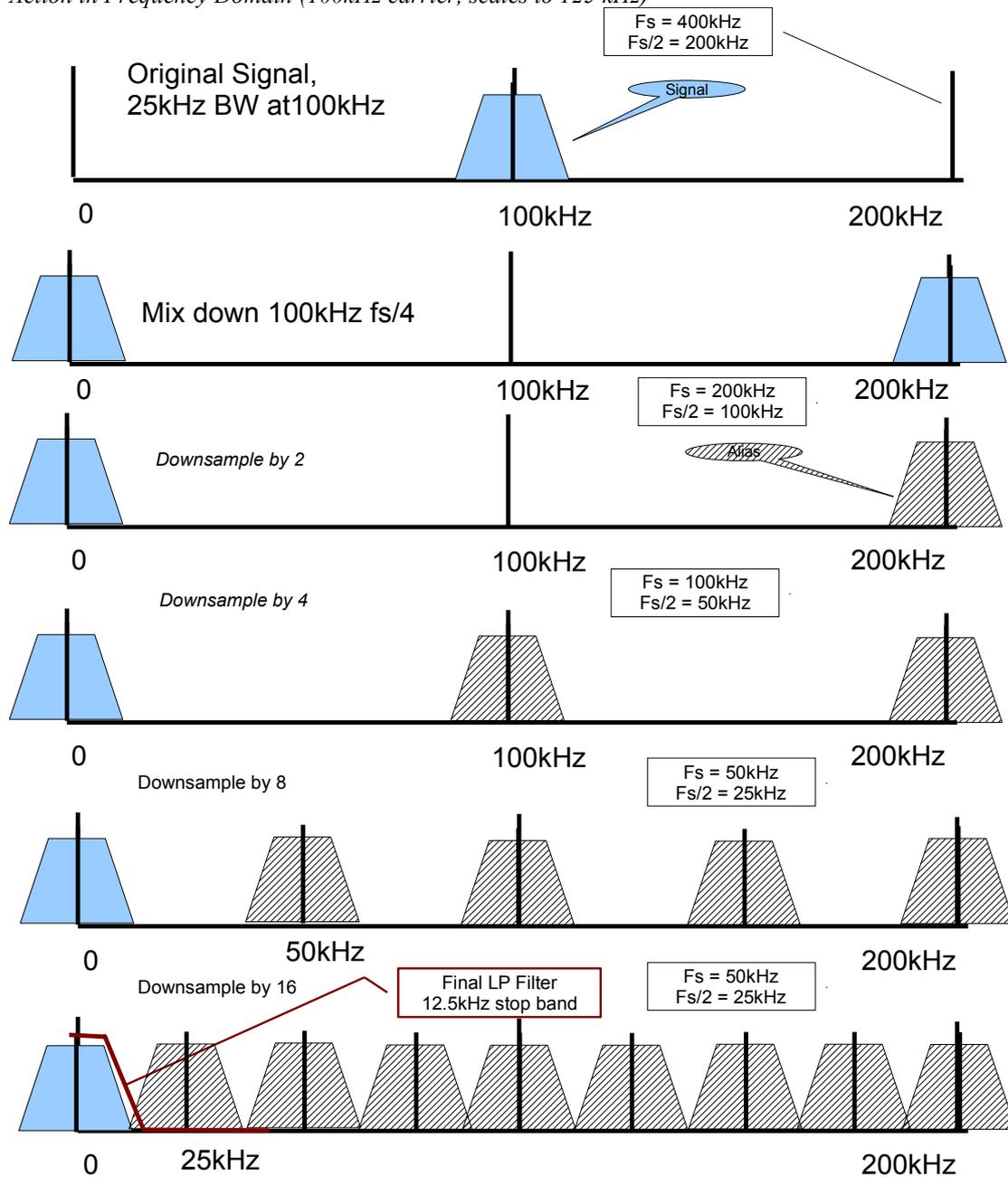
Multi-Channel Digital Down-Converter reduces Output Data Rate



Block Diagram



Action in Frequency Domain (100kHz carrier, scales to 125 kHz)



Product Details

- ACQ196PCI - 96 channel, 16 bit simultaneous digitizer.
- Xilinx SPARTAN 3 FPGA with 2,000,000 gates.
- 32 bit embedded processor subsystem running Linux.
- The core logic design was developed by [Steepest Ascent](#).